

ABSTRACT OF THE DISCLOSURE:

**TITLE: SINGLE-TRANSISTOR TWO-RESISTOR CIRCUIT WHICH
 WHICH TRANSLATES TEST SIGNALS TO SELECTABLE
 VOLTAGE LEVELS**

 A signal translator circuit, for use in sending
test signals to an IC-chip in a chip testing system,
includes first and second resistors, and a single
transistor. The transistor has a current channel which
5 is coupled in series with the first resistor between a
source voltage terminal and an output terminal. The
single transistor also has a control lead, for enabling
and disabling the flow of current through the current
channel, which receives an input test signal at voltage
10 levels that are to be translated. The output terminal is
coupled through the second resistor to a reference
voltage terminal. The output terminal is also coupled to
an input terminal of a socket which is structured to hold
the IC-chip.